

GP1A67L/GP1A67H

Subminiature **OPIC**
Photointerrupter

■ Features

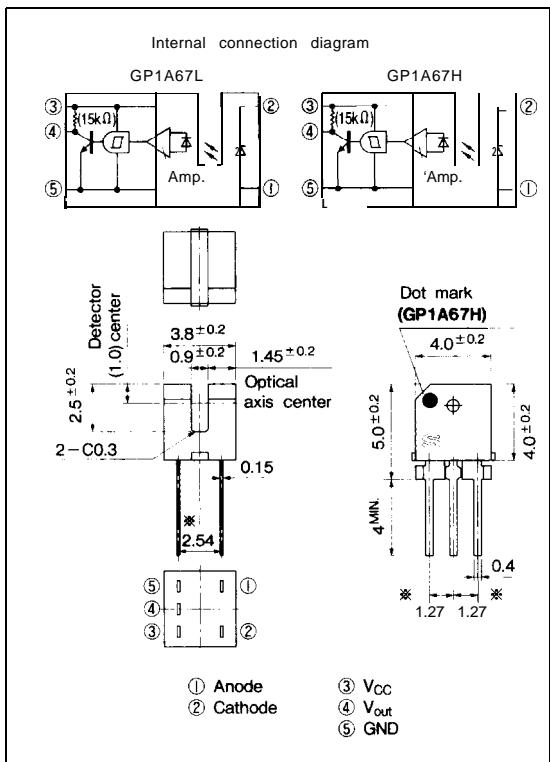
1. Ultra-compact (3.8X 4.0X 4.0mm)
2. TTL compatible output
3. Low operating voltage, low dissipation current suitable for battery-driven applications (Vcc : 2.2 to 7.0V, I_{CC} : TYP. 1.3mA)

■ Applications

1. Compact personal OA equipment
2. Floppy disk drives
3. Auto-focus cameras
4. VCRs

■ Outline Dimensions

(Unit : mm)



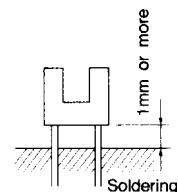
* "OPIC" (Optical IC) is a trademark of the SHARP Corporation
An OPIC consists of a light-detecting element and signal processing circuit integrated onto a single chip.

*The dimensions indicated by * refer to those measured from the lead base.

■ Absolute Maximum Ratings

	Parameter	Symbol	Rating	Unit
Input	Forward current	I _F	50	mA
	Reverse voltage	V _R	6	v
	Power dissipation	P	75	mW
Output	Supply voltage	V _{CC}	7	v
	Output current	I _O	8	mA
	Power dissipation	P _O	80	mW
Operating temperature		T _{opr}	-25 to +85	°C
Storage temperature		T _{stg}	-40 to +100	°C
*'Soldering temperature		T _{sol}	260	°C

*1 For 5 seconds



■ Electro-optical Characteristics

(Ta = 25°C)

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit		
Input	Forward voltage	V _F	I _F =20mA	—	1.2	1.4	v		
	Reverse current	I _R	V _R =3V	—	—	10	μA		
output	Operating supply voltage		V _{CC}	2.2		7.0	v		
	Low level output voltage	V _{OL}	V _{CC} =5V, I _O =4mA, I _F =5mA	0.15	0.4	0.4	v		
			V _{CC} =5V, I _O =4mA, I _F =0						
	High level output voltage	V _{OH}	V _{CC} =5V, I _F =0	4.9	—	—	v		
			V _{CC} =5V, I _F =5mA						
Transfer characteristics	Low level supply current	I _{CL}	V _{CC} =5V, I _F =5mA	1.3	3.8	3.8	mA		
			V _{CC} =5V, I _F =0						
	High level supply current	I _{CH}	V _{CC} =5V, I _F =0	1.0	3.0	3.0	mA		
			V _{CC} =5V, I _F =5mA						
Response time	“High”→“Low” threshold current	GP1A67L	I _{FHL}	V _{CC} =5V	—	0.9	2.5	mA	
	“Low”→“High” threshold input current	GP1A67H	I _{FLH}			—	—		
	Hysteresis	GP1A67L	I _{FHL} /I _{FLH}	V _{CC} =5V	0.55	0.8	0.95		
		GP1A67H	I _{FLH} /I _{FHL}						
Transfer characteristics	“Low”→“High” propagation delay time	GP1A67L	t _{PLH}	V _{CC} =5V	—	9.0	30	μs	
	“Low”→“High” propagation delay time	GP1A67H	t _{PLH}			—	3.0		
	“High”→“Low” propagation delay time	GP1A67L	t _{PHL}	I _F =5mA	—	3.0	15		
	“High”→“Low” propagation delay time	GP1A67H	t _{PHL}			—	9.0		
Response time	Rise time		t _r	R _L =1.2kΩ	—	0.1	0.5		
	Fall time		t _f			—	0.05		

*2 I_{FHL} represents forward current when output changes from “High” to “LOW”*3 I_{FLH} represents forward current when output changes from “LOW” to “High”*4 Hysteresis stands for I_{FHL}/I_{FLH}(GP1A67L) or I_{FLH}/I_{FHL}(GP1A67H).

*5 Test circuit for response time shall be shown below.

Test Circuit for Response Time

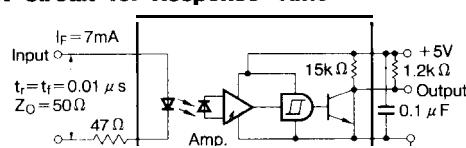


Fig. 1 Forward Current vs. Ambient Temperature

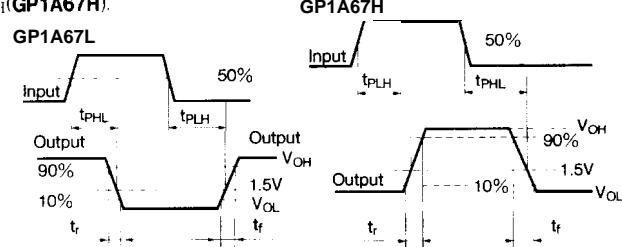
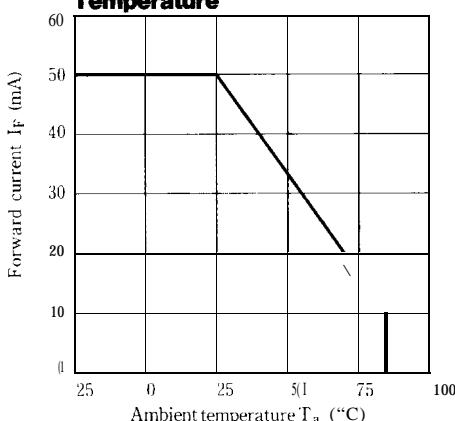


Fig. 2 Power Dissipation vs. Ambient Temperature

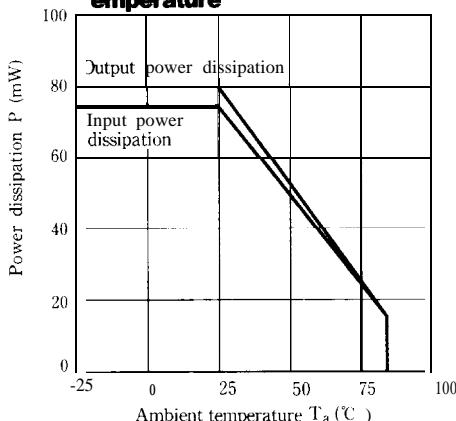


Fig. 3 Low Level Output Current vs. Ambient Temperature

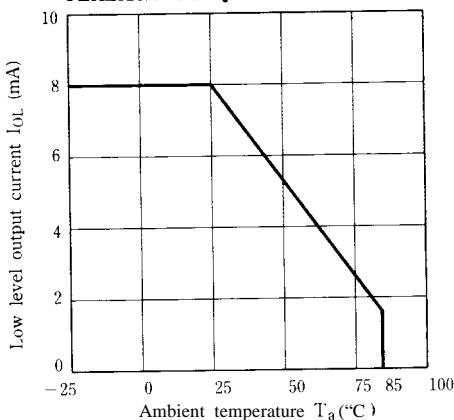


Fig. 5 Relative Threshold Input Current vs. Supply Voltage

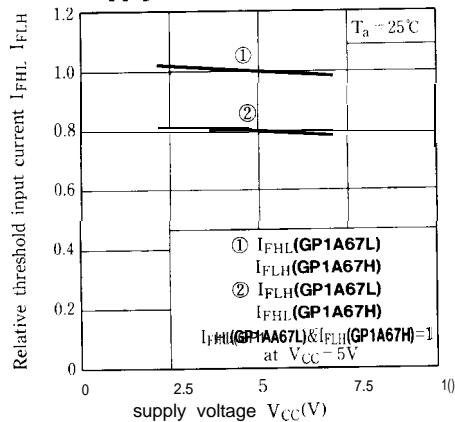


Fig. 7 Low Level Output Voltage vs. Low Level output Current

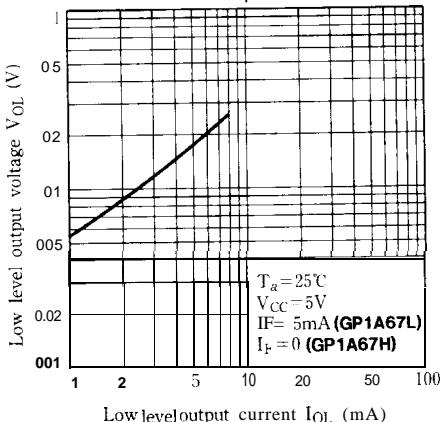


Fig. 4 Forward Current va. Forward Voltage

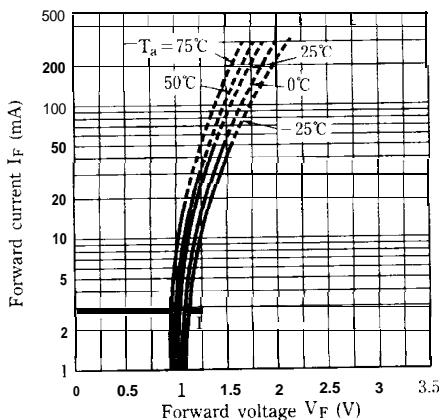


Fig. 6 Relative Threshold Input Current vs. Ambient Temperature

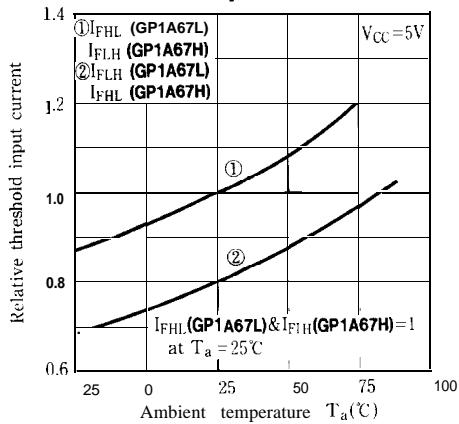


Fig. 8 Low Level Output Voltage vs. Ambient Temperature

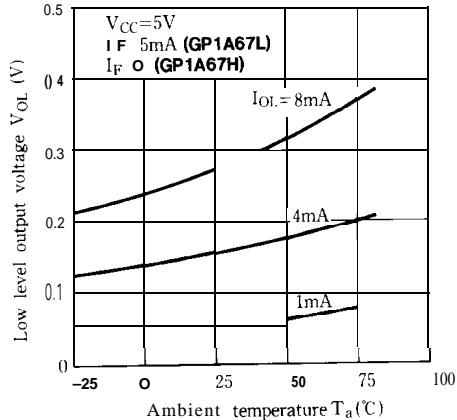


Fig. 9 Low Level Supply Current vs. Supply Voltage

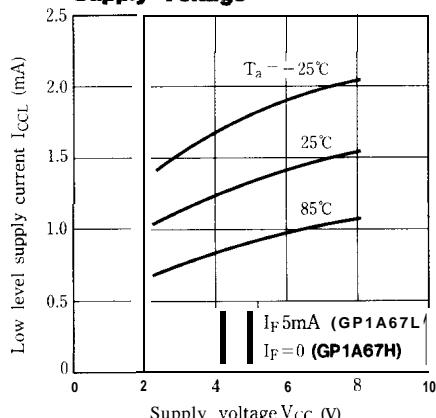


Fig.11 Propagation Delay Time vs. Forward Current

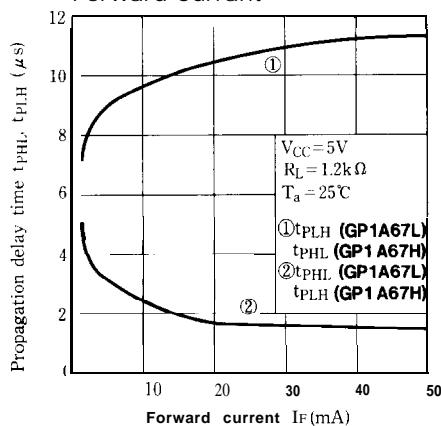


Fig.10 High Level Supply Current vs. Supply Voltage

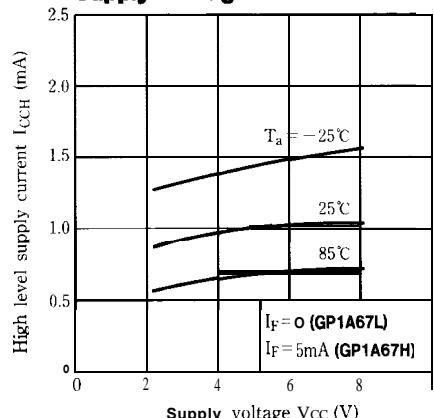
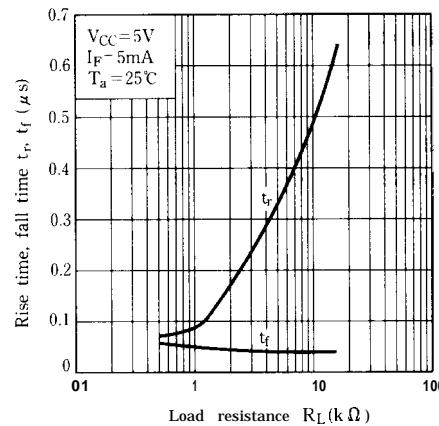


Fig.12 Rise Time, Fall Time vs. Load Resistance



■ Precautions for Use

- (1) In order to stabilize power supply line, connect a by-pass capacitor of more than $0.1 \mu\text{F}$ between V_{CC} and GND near the device.
- (2) Ultrasonic cleaning is prohibited.
- (3) As for other general cautions, refer to the chapter "Precautions for Use." (Page 78 to 93)